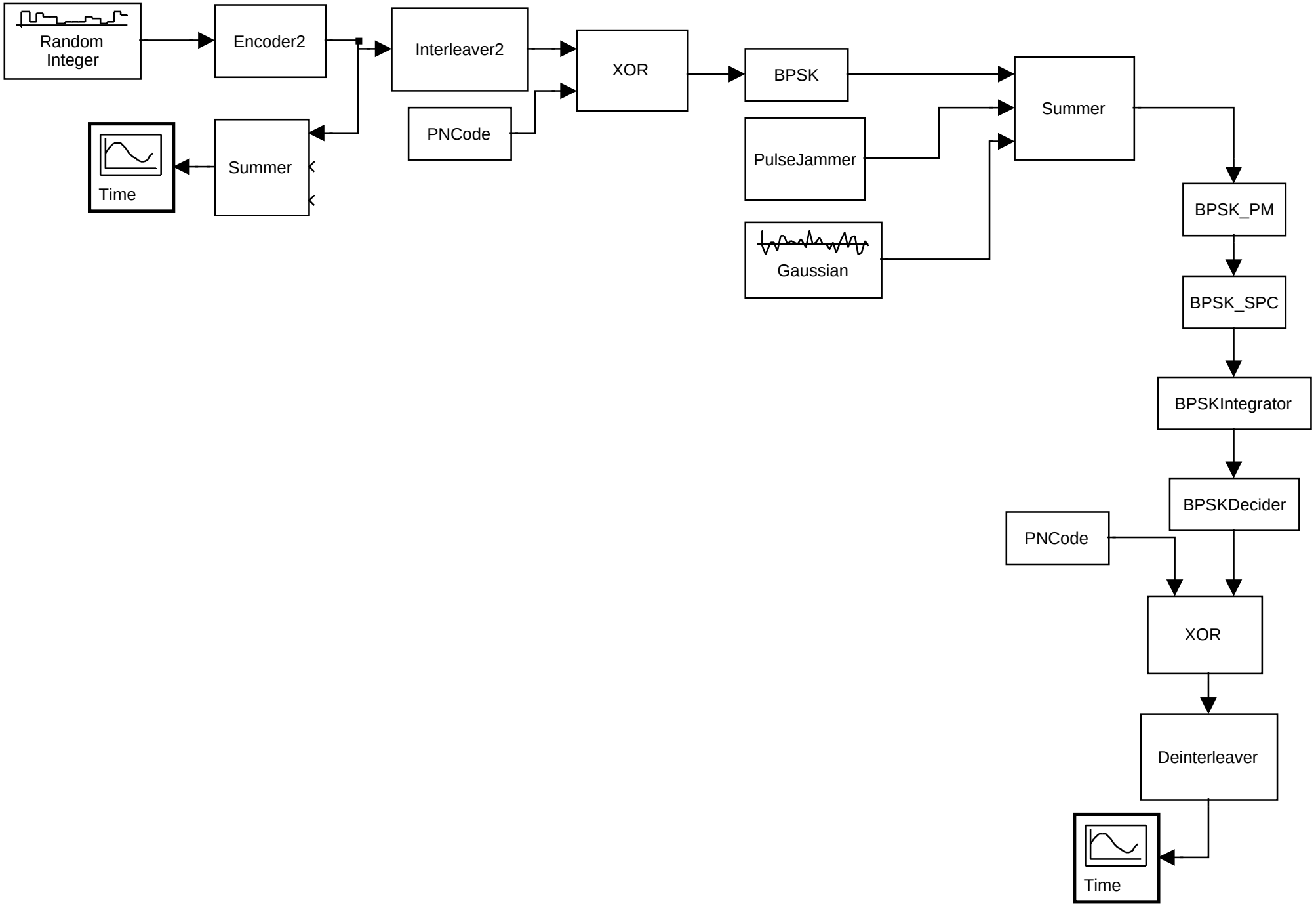
Random Integer
Encoder2
Interleaver2
XOR
BPSK
Summer
PNCode
PulseJammer
Summer
Time
Gaussian
BPSK_PM
BPSK_SPC
BPSKIntegrator
BPSKDecider
PNCode
XOR
Deinterleaver
Time

Description of Blocks:

SI	Block Name	Description
1	Random Integer	Random Bit Generator - Simulink Built In Block
2	Encoder 2	Convolutional Encoder Rate 1/3 - Coded Block
3	Interleaver 2	Convolutional Interleaver - Coded Block
4	PNCode	Generates the Maximum Length spreading sequence using a Linear Feedback Shift Register - Coded Block
5	XOR	This block spreads the coded & interleaved data sequence by a Pseudo Noise sequence - Coded Block
6	BPSK	Generates the BPSK signal for every chip duration - Coded Block
7	STJ	Single Tone Jammer - Coded Block
8	MTJAM	Multi Tone Jammer - Coded Block
9	Pulse Jammer	Pulse Jamming signal - Coded Block
10	Gaussian	Gaussian noise generator - Simulink Built In Block
11	BPSK_PM, BPSK_SPC, BPSKIntegrator, BPSKDecider	The mentioned blocks in cascaded connection simulate a standard Matched Filter followed by a Maximum Likelihood detector for BPSK - Coded Block
12	Deinterleaver	Convolutional Deinterleaver - Coded Block
13	Summer	Block that adds the input signals - Coded Block
14	Time	Block used for data display - Simulink Built In Block