Block ROM Add. — 16K RAM buffer

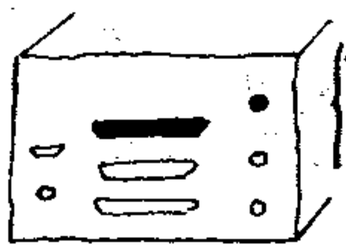
		Mem Add	Port	
①	0000	8032 (L)	19	9000
②	4000	8033 (H)	1A	A---
				B---
				C---

27128 & 27256 V.3.3

EPROM

20BD	60	1D	00			
C0	60	1B	90			
C3	60	1A	C0			
C6	12	1D				
C8	62	32	80			
CB	01	00	90			
CE	02	33	80			
D1	52	80				
D3	05	33	80			
D6	44	00	40			
D9	19	1A	33	80	2F	
DE	19	19	32	80	FF	
E3	16	18	FF			
E6	25	32	80			
E9	21					
EA	4C					
EB	60	1A	C0			
EE	60	1D	00			
F1	FF					

READ BLOCK — Interruptor Abajo



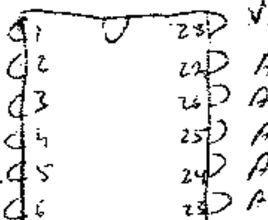
27128	27256
	0000
4000	4000

BS = 12.5 V ← In Voltaje

← Add Rom Comienzo

CS Enable

```
Disable Pw 1
Read CW Port
CS Disable
Let pp = 9000
Let n = (8033)
Let n = n + 80
Let (8033) = n
Buck 16K
  ROM Add H
  ROM Add L
  Add (pp) ← In (18)
  INC Add ROM
  INC pp
  NEXT
  CS = Disable
Pw 1 = OFF
STOP
```



2DF2	60	1D	00			
F5	60	1B	80			
F8	60	1A	80			
FB	12	1D				
FD	62	32	80			
100	01	00	90			
03	02	33	80			
06	52	80				
08	05	33	80			
0B	44	00	40			
0E	19	1A	33	80	FF	
13	19	19	32	80	FF	
18	11	18	FF			
1B	19	1A	33	80	2F	
20	6B	00	1C			
23	19	1A	33	80	FF	
28	25	32	80			
2B	21					
2C	4C					
2D	60	1D	00			
30	FF					

WRITE BLOCK — Interruptor Arriba

27128	27256
	0000
4000	4000

BS = 12.5 V, DE = 21 V ← In Voltaje

← Add ROM Comienzo

22c256

CS = Disable

```
Let n = (8033)
Let n = n + 80
Let (8033) = n
Buck 16K
ROM Add H  Disable
ROM Add L
OUT (18) ← Add (pp)
ROM Add H  Enable
WAIT 40 µs
ROM Add H  Disable
INC Add ROM
INC pp
NEXT
Disable Pw 1
STOP
```

READ Rom Add

2131	60	1D	00	
34	60	1B	90	
32	60	1A	C0	
3A	12	1D		
3C	0C			
3D	ED	1A	FF	
40	12	19		
42	3E	18	FF	
45	4E	3C	21	

4000 (2FFE) — 0000 (2FFF)

Disable PW 1
Read cw Port
$\overline{cs}$ = Disable
B5 = 12.5V ← In Voltaje
In Add H
Port
In Add L
Display Read DATA
JP 213C

COPY RUTINE

2148	40	4C	21	
4B	FF			
4C	2A	32	80	
4F	ED	5B	34	80
53	ED	4B	36	80
57	ED	B0		
59	C9			

(8032) = Add Origen
(8034) = Add Destino
(8036) = Deep

VSR 214C
STOP
LD HL, (8032)
LD DE, (8034)
LD BC, (8036)
LDIR
RET

CLEAR RAM

215A	01	00	90	
5D	02	FF		
5F	21			
60	2F	00	D0	FF
64	4E	ED	21	

@ 9000 → CFFF = FF

Let pp = 9000
Let Add(pp) = FF
INC pp
IF pp = D000 then STOP
JP 215D

GEN 256K ROM

2162	60	1D	00			
6A	60	1B	90			
6D	60	1A	C0			
70	60	1D	B5			
73	0B	32	80	00	00	
78	19	1A	33	80	2F	
7D	19	19	32	80	FF	
82	25	32	80	4E	28	21

Disable PW 1
Read cw Port
$\overline{cs}$ Disable
Voltaje = 12.5V
Add (8032) = 0000
Add ROM H
Add ROM L
INC Add ROM & Jump

(V.3.33) **BASIC MONITOR Voltaje IN**

2188	CD	48	00
8B	3A	1F	80
8E	32	0E	80
91	CD	F5	05
94	CD	BC	01
97	18	EF	

2199