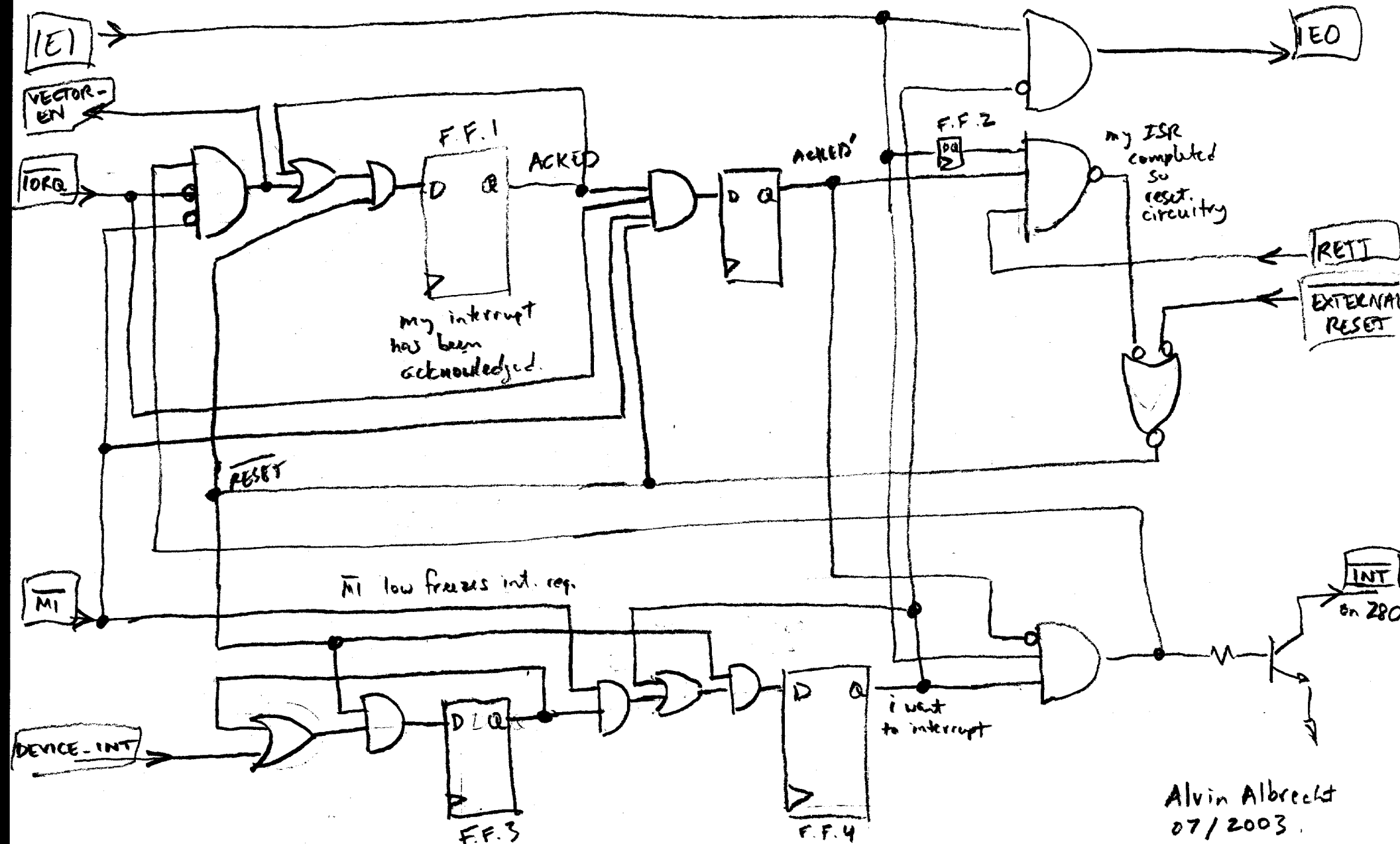
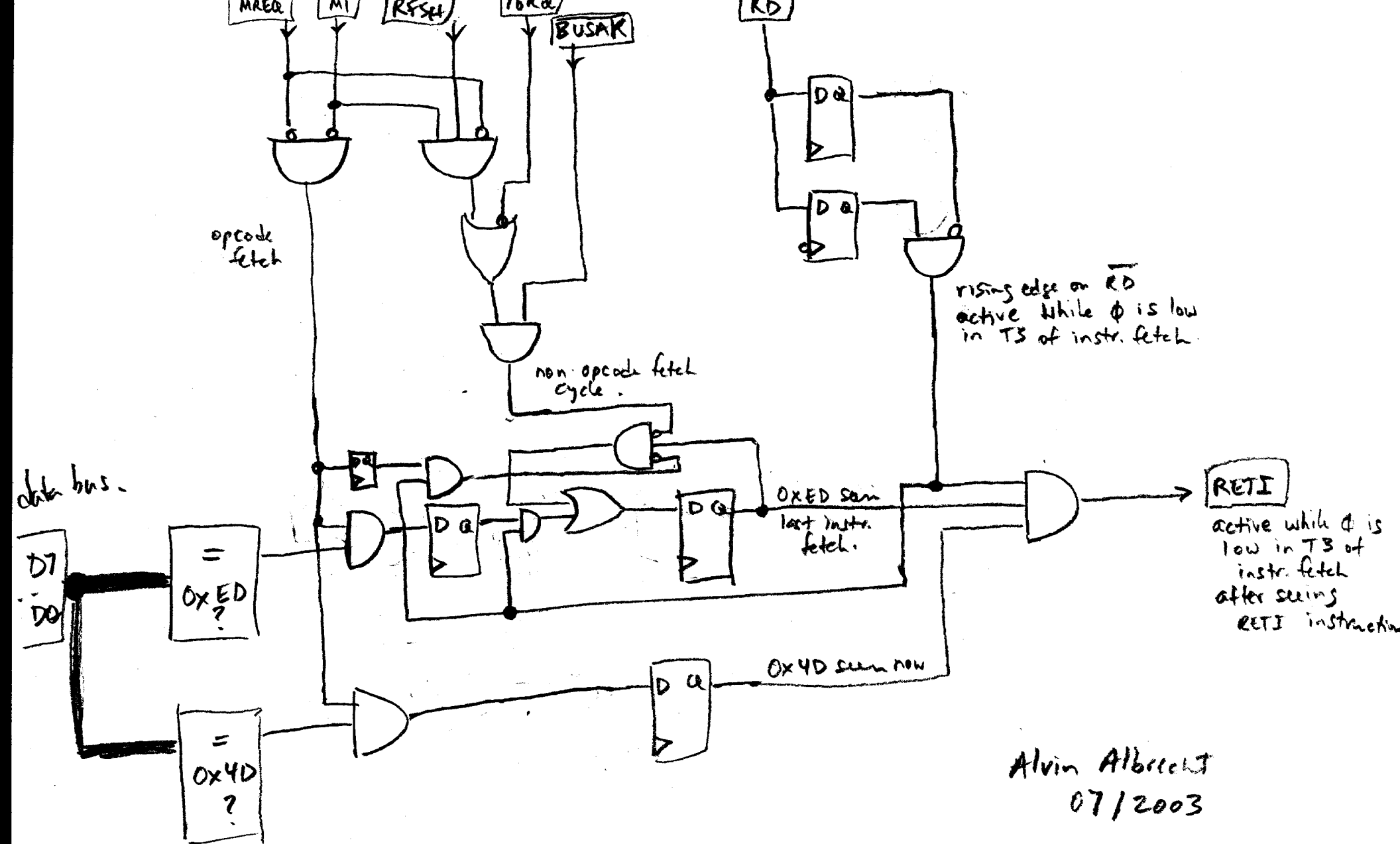




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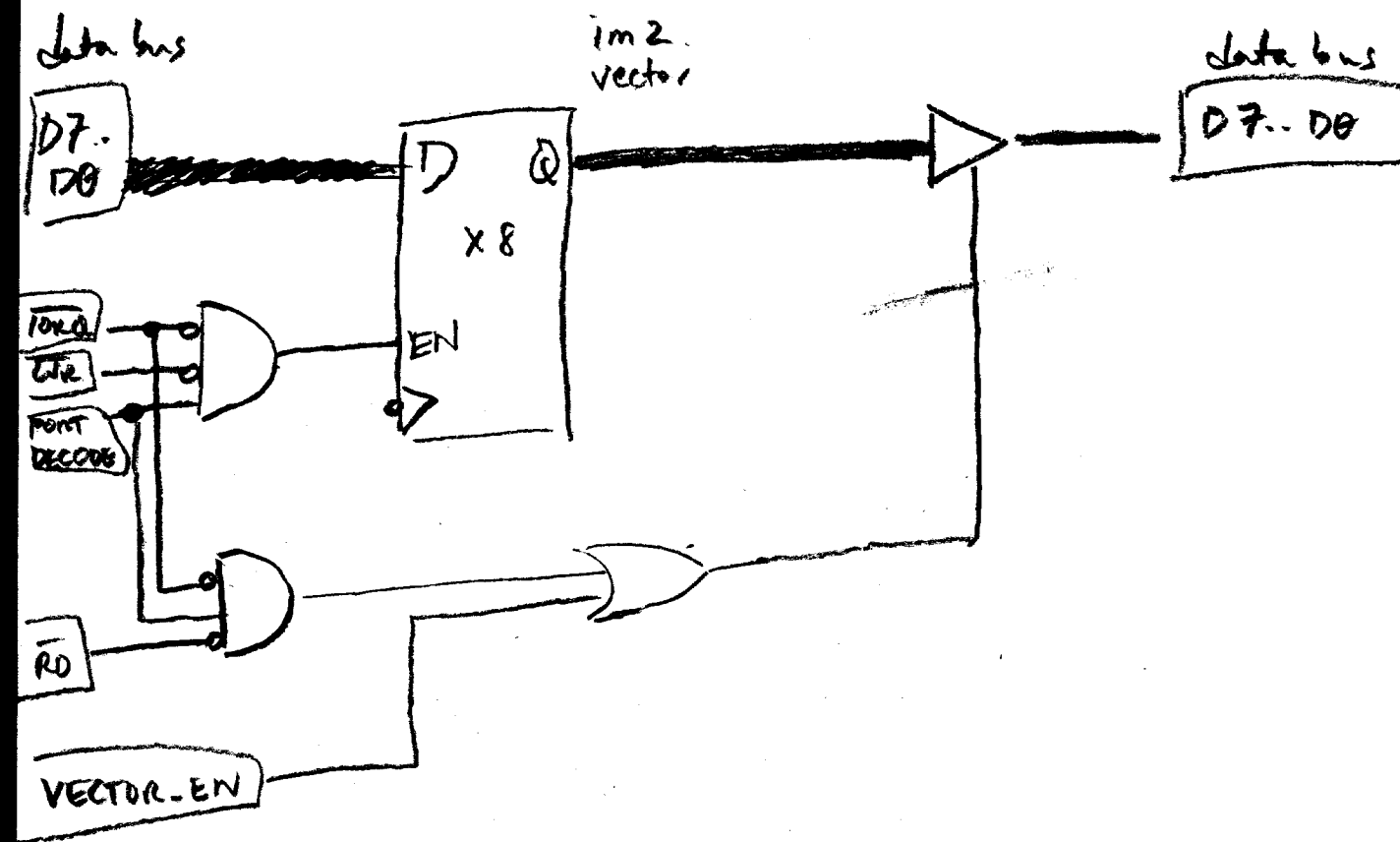
IM2 INTERRUPT CIRCUIT - ONE PER INTERRUPTING DEVICE

- device signals interrupt by pulsing DEVICE-INT high for > one cycle.
- EXTERNAL RESET must be held low for > one cycle to reset circuit.



RETI opcode IDENTIFICATION - can be shared by all int. devices.

- **RETI** will go high while ϕ is low during T3 of instruction opcode fetch after seeing a RETI instruction
- Attach external reset to FFs.



IM2 VECTOR - ONE PER INTERRUPTING DEVICE

- Vector is full 8-bits so that it can store RST instructions for IM0.
- Could use bottom bit as an INT enable.
- Define PORT DECODE signal true when A7..A0 match i/o port assigned.

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