`CURRICULUM VITAE

OF

Dr. Fathi A. Farag.

Date: December 2003

I. GENERAL

Name	**Fathi Abdel-Fattah Farag Ahmed (F. A. FARAG)**	
Date of Birth **Place of Birth** **Nationality**	October 21, 1963. Sharkia, Zagazig Egypt. Egyptian.	
Office	Department of Electronic and communication, Faculty of Engineering, Zagazig University Zagazig, Egypt E-mail: fathi@ieee.org Fax: + 20-55-2304987 Phone: + 20-55-3665774 (home).	
Present Occupation	Lecturer	
Interested areas	• **Microelectronic circuit analysis and design.** • **Mixed Analog/Digital IC design, layout and simulation.** • **Device modeling (MOST, Bipolar,…..).** • **CMOS and BiCMOS circuit design.** • **Sample/data circuit analysis and design.** • **RF electronic design.** • **VHDL and Verling Language.**	
Academic Qualifications	***B. Sc.***	Electrical Engineering, Communications & Electronics, Assiut University, May 1986, Average Grade: Very Good, *B.Sc*. Project : Grade of Distinction with title "**Photovoltic Solar cell array control**"
	M. Sc.	Electrical Engineering, Assiut University (1991), ***Title***: ***"Insensitive Switched capacitor realization of zero producing Sections"***
	Ph. D	Electrical Engineering dept., The Federal University of Santa-Catarina (UFSC), Brazil (June 1999), ***Title***: **"*Digitally programmable low-voltage switched-current filters"***

II. EMPLOYMENT HISTORY

1. **Demonstrator**, Electrical Eng. Dept., Assiut University (from 1987 to 1991)
2. **Assistant Lecturer**, Electrical Eng. Dept., Assiut University (from -1991 to February 19-2000).
3. **Lecturer**
   - February, 20-2000 to 2002 in Electrical Eng. Dept., Assiut University
   - From January 2003 In Electronic and Communication Dept., Faculty of Eng. Zagazig University

III. PROFESSION ACTIVITY

- Member of IEEE
- Member of reviewer committee of the 5th world Multi-Conference on Systemics, Cybernetics and Informatics (SCI 2001).
- CO-operation with MOSIS.

IV. EXPERIENCES

- **Project Supervisor.**
  - Project of " **Speech synthesis**", October, 1988 to Jun, 1989, (Co-Supervisor).
  - Project no. CB/90038 USA/Egypt, which has a title " **Improving Service on Telephone Communication Through Introducing High Security Non- interferable Communication Channels"**, May, 1990 to Oct., 1991 (Co-Supervisor).
  - Project of " **Digital Filter design and Realization**", October, 1990 to Jun, 1991, (Co-Supervisor).
  - Project of "**Error Coding detection and correction**", October, 1991 to Jun, 1992, (Co-Supervisor).
  - Project of "**Robot Simulation**", October, 1992 o Jun, 1993 (Co-Supervisor).
  - Project of "**Adaptive System Identification**", October, 1993to Jun, 1994 (Co-Supervisor).
  - Project of "**Programmable gain amplifier cell for VLSI application**", October, 1999 to Jun, 2000 (Supervisor).
  - Project of "**Digitally Programmable cell in CMOS process**", October, 2000 to Jun, 2001, (Supervisor).
  - Project with cooperation with MOSIS Fabrication company "**CMOS Digitally controlled amplifier in SCNE technology (1.5um) From MOSIS**", October, 2001 to Jun, 2002, (Supervisor).
    - Through this project we have designed a complete digital controlled system chip.
  - Project of "**ASIC- Traffic light driver**", October, 2002 to Jun, 2003, (Supervisor).
    - In this project we have designed traffic light driver chip (not fabricated –layout) as Application-Specific Integrated Circuits (ASIC).
- **Teaching Courses :**

1. **Electronic fundamental**
2. **Electronic circuit design.**
3. **VLSI fundamental.**
4. **Integrated circuit design.**

V. LIST OF PUBLICATIONS

1-M. F. FAHMY , A. EL-Wardaney, G. A Raheem and **F. A. FATTAH " Low Sensitivity SC Realization of Brune & C-type Reactance Sections**" Int. Journal of Circuit Theory & Appl. Vol. 19, NO. 5, PP. 459-470, Sept-October 1991.

2-M. F. FAHMY , M. M. DOSS and **F. A. FATTAH "Low sensitivity SC Implementation of Reactance E-type Zero Producing Section**" IEEE Trans. On Circuits and System part-I, Vol. 39, No. 1,pp. 59-63, January 1992.

3- A. EL-Wardaney, G. A Raheem, M. Abou-Zahhad and **F. A. FATTAH " Insensitive SC implementation of Reactance Zero Producing Sections**" Bulletin of the Faculty of Eng., Vol. 19, January 1991, Part 1.

4-M. F. Fahmy, A. El-wardaney, G. Abdel-Raheem and **Fathi Abdel-Fattah " Insensitive SC-Realization of Brune & C-type Reactance Sections**" Bulletin of the Faculty of Eng., Vol. 19, July 1991, Part 2.

5- **F. A. Farag** , R. Faustino, S. N. Filho, C. Galup-Montoro and M. C. Schneider " **A programmable second generation SI integrator for low voltage applications** " International VLSI 97 Conf., pp. 129-139, Gramado ,Brazil, August. 1997.

6- R. L. Oliveiro Pinto, **F. A. Farag ,** M. C. Schneider and C. Galup-Montoro " **A design methodology for MOS amplifiers** " SBCCI 97 Conf., pp. 233-242, Gramado, Brazil, August. 1997.

7- **F. A. Farag**, C. Galup-Montoro and M. C. Schneider " **A programmable low voltage switched-current FIR filter for disk drive equalization,**" International Conference on Microelectronics and Packaging (ICMP'98), pp. 249-255, PR, Brasil, August, 1998.

8- F. A. Farag, C. Galup-Montoro and M. C. Schneider " **A programmable low voltage switched-current FIR filter,** " Proc. ISCAS, Vol. II , pp. 472-475, May 1999.

9- F. A. Farag, M. C. Schneider and C. Galup-Montoro "**A fully balanced switched-current sample-hold amplifier for low-voltage application,**" International Conference on Microelectronics and Packaging (ICMP'99), pp. 9-12, Campinas, SP Brazil, August, 1999.

10- F. A. Farag, C. Galup-Montoro and M. C. Schneider" **Digitally programmable switched-current FIR filter for low-voltage application**" IEEE Journal of Solid-State Circuits, Vol. 35, Pp. 637-641, April, 2000

11- F. A. Farag, C. Galup-Montoro and M. C. Schneider" **A Fully balanced programmable sample-hold amplifier for low-voltage application**" Proc. ISCAS, Vol. III, pp. 443-446, May 2000.

12- L. C. Marques, C. Galup-Montoro, S. N. Filho, **F. A. Farag**, W. Serdijn and M. C. Schneider "**Switched-Mosfet Sampled-Data Technique For Low-Voltage Supply**" Tutorial Guide: IEEE ISCAS, pp. 1.5.1-1.5.6, May 2001.

13-F. A. Farag, " **CMOS Low-Voltage Switched-Mosfet Cyclic Analog-to-Digital converter**" Bulletin of the Faculty of Eng., Assiut University, Vol. 29, No. 3(2/2), pp. 357-363, September 2001.

14- H. Hamad, **F. A. Farag,** and M. S. A. El-hakeem**, " A New wideband BiCMOS Four-Quadrant Analog Multiplier ",** Published in IEEE **ISCAS** , Vol. I, pp. 729-732, May 2002.

15-F. A. Farag, "CMOS Amplifier Design Methodology for Optimum Slew Rate ", IEEE 11th Mediterranean Electrotechincal Conference (MELECON 2002) pp.532-536, Cairo May 2002.

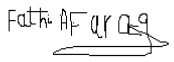
16-F. A. Farag, "Low-Voltage ADC for sample to serial interface application" " The 15th IEEE Symposium of Integrated Circuits and Systems Design, pp. 258-261, Porto-Alegre, Brazil September, 2002.

17-F. A. Farag "CMOS Digitally Controlled Amplifier for Low Voltage Applications " IEEE 15th International Conference of Microelectronic (**ICM 2003),** pp. 2-5, Cairo, Egypt, December 2003.

18- M. Farahat, **F. A. Farag**, and H. El Samary "**Digital only Technology Analog –to-Digital Converter Circuit**'' The 46th IEEE Midwest Symposium on Circuits and Systems, Cairo, Egypt, December 2003.

19- F. A. Farag, "Low Voltage High Accuracy Current Copier Cell," The 46th IEEE Midwest Symposium on Circuits and Systems, Cairo, Egypt, December 2003.

Dr. Fathi A. Farag



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